

<u> 10092545.030602</u>

Title: System And Method For Dynamic Processor Core And Cache Partitioning On

Large-Scale Multithreaded, Multiprocessor Integrated Circuits

Inventor: Donald C Soltis -- Docket No. 10016693 -- William J. Streeter (970) 898-3886

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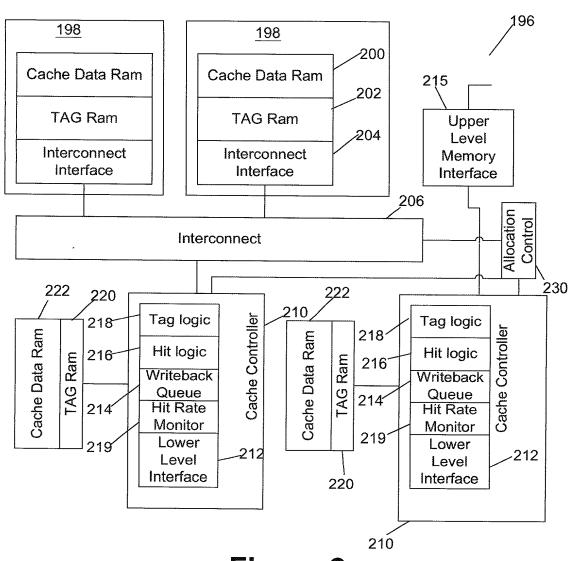


Figure 2

Title: System And Method For Dynamic Processor Core And Cache Partitioning On Large-Scale Multiprocessor Integrated Circuits

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